

TO: Mail Stop 8 Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450	REPORT ON THE FILING OR DETERMINATION OF AN ACTION REGARDING A PATENT OR TRADEMARK
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In Compliance with 35 U.S.C. § 290 and/or 15 U.S.C. § 1116 you are hereby advised that a court action has been filed in the U.S. District Court Southern District of New York on the following

Trademarks or Patents. (the patent action involves 35 U.S.C. § 292.)

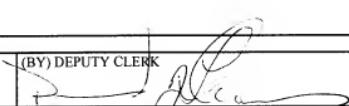
DOCKET NO. 07CV816 (AKH)	DATE FILED 2/2/2007	U.S. DISTRICT COURT Southern District of New York
PLAINTIFF ANVIK CORPORATION		DEFENDANT LG PHILIPS LCD CO., LTD., ET AL.,
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
1 See Attached Sheet		See Attached Sheet
2 <u>4,924,257</u>		
3 <u>5,285,236</u>		
4 <u>5,291,240</u>		
5		

In the above—entitled case, the following patent(s)/ trademark(s) have been included:

DATE INCLUDED	INCLUDED BY	<input type="checkbox"/> Amendment <input type="checkbox"/> Answer <input type="checkbox"/> Cross Bill <input type="checkbox"/> Other Pleading
PATENT OR TRADEMARK NO.	DATE OF PATENT OR TRADEMARK	HOLDER OF PATENT OR TRADEMARK
1 See Attached Sheet		See Attached Sheet
2		
3		
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5		

In the above—entitled case, the following decision has been rendered or judgement issued:

DECISION/JUDGEMENT
COPY ATTACHED: JUDGMENT

CLERK Ruby J. Krajick	(BY) DEPUTY CLERK 	DATE 4/6/2012
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Copy 1—Upon initiation of action, mail this copy to Director Copy 3—Upon termination of action, mail this copy to Director
 Copy 2—Upon filing document adding patent(s), mail this copy to Director Copy 4—Case file copy

UNITED STATES DISTRICT COURT
SOUTHERN DISTRICT OF NEW YORK

USDC SDNY
DOCUMENT
ELECTRONICALLY FILED
DOC #:
DATE FILED: 4/5/12

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ANVIK CORPORATION, :
Plaintiff, :
v. :
NIKON PRECISION, INC., et al., : Civ. No. 05-7891 (AKH)
LG.PHILIPS LCD CO., LTD., et al., : Civ. No. 07-0816 (AKH) ←
SAMSUNG ELECTRONICS AMERICA, INC., et al., : Civ. No. 07-0818 (AKH)
CHI MEI OPTOELECTRONICS, et al., : Civ. No. 07-0821 (AKH)
AU OPTRONICS CORP., et al., : Civ. No. 07-0822 (AKH)
SHARP CORP., et al., : Civ. No. 07-0825 (AKH)
INNOLUX DISPLAY CORP., : Civ. No. 07-0826 (AKH)
HANNSTAR DISPLAY CORP., : Civ. No. 07-0827 (AKH)
AFPD PTE LTD., and : Civ. No. 07-0828 (AKH)
IPS ALPHA TECHNOLOGY, LTD, et al. : Civ. No. 08-4036 (AKH)
Defendants. :
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JUDGMENT

Upon the Court's Order of April 4, 2012 (the "Order"), it is hereby ordered that the above-captioned actions are dismissed upon the following terms:

1. Claims 17 and 18 of U.S. Patent No. 4,924,257 are declared invalid for failure to comply with the requirement that "[t]he specification . . . shall set forth the best mode contemplated by the inventor of carrying out his invention." 35 U.S.C. § 112.

2. Claims 23 and 25 of U.S. Patent 5,285,236 are declared invalid for failure to comply with the requirement that “[t]he specification . . . shall set forth the best mode contemplated by the inventor of carrying out his invention.” 35 U.S.C. § 112.

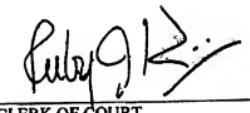
3. Claim 25 of U.S. Patent 5,291,240 is declared invalid for failure to comply with the requirement that “[t]he specification . . . shall set forth the best mode contemplated by the inventor of carrying out his invention.” 35 U.S.C. § 112.

4. Plaintiff's Complaint or Amended Complaint, as the case may be, in each of the above-captioned actions is dismissed with prejudice.

5. This is without prejudice to Plaintiff's right to appeal.

6. Defendants' Counterclaims, other than those asserting invalidity of U.S. Patents Nos. 4,924,257, 5,285,236, and 5,291,240 in each of the above-captioned actions, are dismissed without prejudice as moot.

April 5, 2012



CLERK OF COURT

TABLE I

D_s = RESIST SENSITIVITY (mJ/cm ²)	10			50			100		
f = LASER PULSE REPETITION RATE (Hz)	303 204 98			303 204 98			303 204 98		
N = NUMBER OF OVERLAPPING PULSES	37 25 12			37 25 12			37 25 12		
E_w = ENERGY DONS. / PULSE AT WAFER (mJ/cm ²)	0.27 0.40 0.83			1.35 2.00 4.17			2.70 4.00 8.33		
e_w = ENERGY PER PULSE AT WAFER (mJ)	0.35 0.52 1.08			1.76 2.60 5.42			3.51 5.20 10.8		
P_w = POWER INCIDENT ON WAFER (mW)	106 106 106			530 530 530			1060 1060 1060		
P_L = LASER POWER (W)	0.53 0.53 0.53			2.65 2.65 2.65			5.30 5.30 5.30		

FIG.10

TABLE II. WAFER THROUGHPUT CALCULATION

WAFER DIAMETER (mm)	CHIP SIZE (mm x mm)	NUMBER OF CHIPS ON WAFER	THROUGHPUT (WAFERS/HR)		
			EVERY SITE ALIGN	EVERY 4th SITE ALIGN	EVERY 10th SITE ALIGN
125	10.6 x 22	52	68.7	97.8	105.9
150	10.6 x 22	75	54.0	81.3	90.2
200	10.6 x 22	134	34.9	57.0	65.2

FIG.11

United States Patent

[19] Jain

US005285236A

[11] Patent Number: 5,285,236

[45] Date of Patent: Feb. 8, 1994

[54] **LARGE-AREA, HIGH-THROUGHPUT,
HIGH-RESOLUTION PROJECTION
IMAGING SYSTEM**

[76] Inventor: Kanti Jain, 18 Algonquian Trail,
Briarcliff Manor, N.Y. 10510

[21] Appl. No.: 954,662

[22] Filed: Sep. 30, 1992

[51] Int. Cl. 5 G03B 27/53

[52] U.S. Cl. 355/53, 355/67

[58] Field of Search 355/53, 66, 67

[56] **References Cited**

U.S. PATENT DOCUMENTS

4,891,663	1/1990	Hirose	355/53
4,924,257	5/1990	Jain	355/53
5,166,306	12/1992	Morimoto et al.	355/53
5,232,838	7/1993	Nakanishi et al.	355/53
5,227,839	7/1993	Allen	355/53

Primary Examiner—Michael L. Gellner

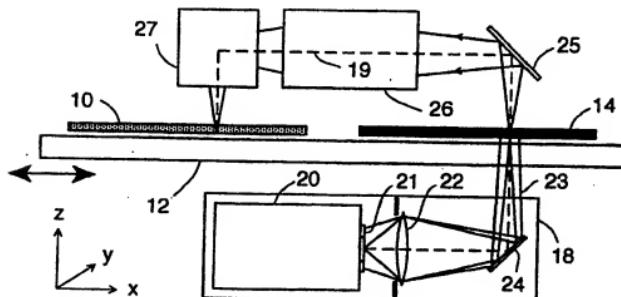
Assistant Examiner—D. P. Malley

[57] **ABSTRACT**

This projection imaging system has large-area exposure capability, high exposure throughput, and high resolu-

tion, and comprises: (a) a stage for holding in fixed juxtaposition a mask and a substrate, and capable of scanning in one dimension, and when not scanning in that dimension, capable of moving laterally in a direction perpendicular to the scan direction so as to position itself for another scan, the stage exposing the full substrate by breaking up the substrate area into parallel strips, and exposing each strip by scanning the length of the strip across a fixed illumination region; (b) an illumination system having an effective source plane of a predetermined shape, and capable of illuminating on the mask a region of the above predetermined shape; (c) a projection assembly having an object-to-image magnification ratio of unity, having means to render the image in the same orientation as the object, and having an image field of the above predetermined shape and of an area smaller than the substrate area; and (d) provision for additive illumination in overlap regions of areas exposed by adjacent scans such that the effect of the exposure dose delivered in the overlap regions is seamless and the effect of the exposure dose delivered across the entire substrate is uniform.

28 Claims, 5 Drawing Sheets



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United States Patent [19]
Jain

US005291240A

[11] Patent Number: 5,291,240
[45] Date of Patent: Mar. 1, 1994

Primary Examiner—Richard A. Wintercorn
Attorney, Agent, or Firm—Carl C. King

[57] ABSTRACT

This patterning system has the ability to uniformly image a mask onto a substrate having nonlinear exposure characteristics, has large-area exposure capability, and comprises: (a) a stage system capable of synchronously scanning a mask and a substrate in one dimension, and when not scanning in that dimension, capable of moving them laterally in a direction perpendicular to the scan direction so as to position the mask and substrate for another scan partially overlapping the preceding scan, thus exposing the full substrate in an overlapping scan-and-repeat fashion; (b) an illumination system capable of illuminating on the mask a region of a predetermined multi-sided shape which has at least one of its sides curved, the curvatures of said curved sides being so determined that adjacent scanning exposures are compensated in their overlap regions for the nonlinear response characteristics of the substrate so as to provide uniform cumulative response; (c) a projection assembly capable of imaging the illuminated region on the mask onto the substrate, having the desired imaging resolution, and having an image field size smaller than the substrate; and (d) provision for adjusting the widths of the overlap regions of adjacent scans in such a way that the cumulative response over the entire substrate is uniform.

[54] NONLINEARITY-COMPENSATED
LARGE-AREA PATTERNING SYSTEM

[75] Inventor: Kanti Jain, Briarcliff Manor, N.Y.

[73] Assignee: Anvik Corporation, Elmsford, N.Y.

[21] Appl. No.: 967,189

[22] Filed: Oct. 27, 1992

[51] Int. Cl. G03B 27/42

[52] U.S. Cl. 355/53, 355/77

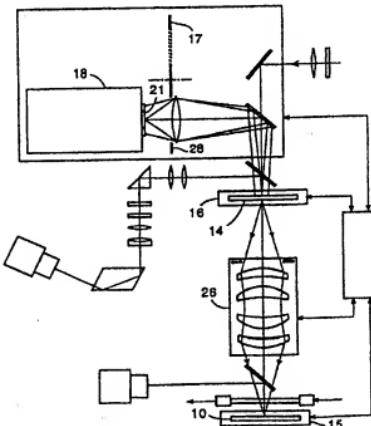
[58] Field of Search 355/50, 51, 53, 77,
250/492.2, 546

[36] References Cited

U.S. PATENT DOCUMENTS

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4,302,079	11/1981	White	350/371
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4,901,109	2/1990	Mitome et al.	355/68
4,924,257	5/1990	Jain	355/53
4,962,318	10/1990	Nishi	250/546
5,227,839	7/1993	Allen	355/53

26 Claims, 6 Drawing Sheets



5



United States Patent [19]

Jain

[11] Patent Number: 5,721,606

[45] Date of Patent: Feb. 24, 1998

[54] **LARGE-AREA, HIGH-THROUGHPUT, HIGH-RESOLUTION, SCAN-AND-REPEAT, PROJECTION PATTERNING SYSTEM EMPLOYING SUB-FULL MASK**

[76] **Investor:** Kanti Jain, 18 Algonquin Trail, Briarcliff Manor, N.Y. 10510

[21] **Appl. No.:** 524,786

[22] **Filed:** Sep. 7, 1995

[51] **Int. Cl. 5** B01L 21/027

[52] **U.S. Cl.** 355/53; 255/77

[58] **Field of Search** 355/50, 53, 54, 355/55, 60, 72, 75

[56] References Cited

U.S. PATENT DOCUMENTS

4,708,466	11/1987	Ishotsu et al.	355/53
5,150,152	9/1992	Ishotsu et al.	355/53
5,285,236	2/1994	Jain	355/53

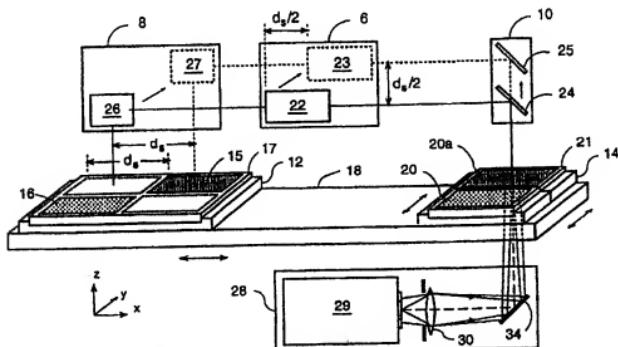
Primary Examiner—John H. Pendegrass
Attorney, Agent, or Firm—Carl C. Kling

[57] ABSTRACT

For high-throughput, low-cost manufacturing of electronic

modules, it is desirable to use a large-format, 1:1-imaging exposure system. In such a system, it is further desirable to have the capability to pattern a large substrate having multiple segments using a mask of the size of one substrate segment. The substrate is mounted on an x-y stage which moves the substrate with respect to the imaging optics, both within a segment and from segment to segment. For each mask position, moving from one substrate segment to another will result in a significant change in the length or orientation of the optical imaging path. Such problems are eliminated by using, in conjunction with the primary x-y stage, an auxiliary stage which maintains the optical parameters essentially constant for the imaging of different substrate segments. The auxiliary stage in a first embodiment is mounted on the primary x-y stage and is deployed to move the mask to compensate for the primary stage motion required to preset the subsequent substrate segment at the imaging location and keep the optical parameters constant. In a second embodiment, the auxiliary stage is configured as an optics stage set mounted orthogonally to the x-y stage and moves components of the projection system to present a different substrate segment at the imaging location while keeping the optical imaging parameters constant. A third embodiment employs auxiliary stages for moving both the mask and the projection optics for greater versatility.

22 Claims, 5 Drawing Sheets



6
US005897986A

United States Patent [19]

Dunn et al.

[11] Patent Number: 5,897,986

[45] Date of Patent: Apr. 27, 1999

[54] PROJECTION PATTERNING OF LARGE SUBSTRATES USING LIMITED-TRAVEL X-Y STAGE

[75] Inventors: Thomas J. Dunn, Mohegan Lake, N.Y.; Nestor O. Farmiga, Cliffon, N.J.; Kanti Jain, Briarcliff Manor, N.Y.

[73] Assignee: Anvik Corporation, Hawthorne, N.Y.

[21] Appl. No.: 08/864,169

[22] Filed: May 28, 1997

[51] Int. Cl. 6 G03F 7/22

[52] U.S. Cl. 436/394; 430/397; 355/77

[58] Field of Search 430/394, 396, 430/397; 438/946; 355/77

[56] References Cited

U.S. PATENT DOCUMENTS

4,775,477 10/1988 Kosugi et al. 355/53
5,285,236 2/1994 Jain 355/53

Primary Examiner—John A. McPherson
Attorney, Agent, or Firm—Carl C. Kling

[57] ABSTRACT

A large-format substrate patterning system, for microelectronics manufacturing, utilizes a substrate docking fixture to enable relative motion between the substrate stage and the substrate. This enables exposure of a large-format substrate which has been partitioned into different modules where each module contains an entire pattern transferred from a mask. This projection system enables patterning of a large multi-module substrate using a stage whose range of travel is smaller than the size of the substrate and using a mask whose area is smaller than the size of the substrate. This is accomplished by repositioning the substrate to expose each module sequentially. In order to reposition the substrate, its location is maintained fixed in space by a substrate docking fixture while the movable stage of the lithography system is repositioned to position a different module of the substrate in the image field of the lithography tool. This allows the use of a mask whose size is determined by the size of each substrate module, and the use of a scanning stage whose required range of travel is determined by the size of the substrate module, and not by the size of the entire substrate. This eliminates the size limitation of the substrate from dependence on the range of travel of the stage, and permits the repetitive use of a small mask or series of small masks to produce a composite multi-module pattern on the substrate.

7 Claims, 12 Drawing Sheets

